

FIG. 1

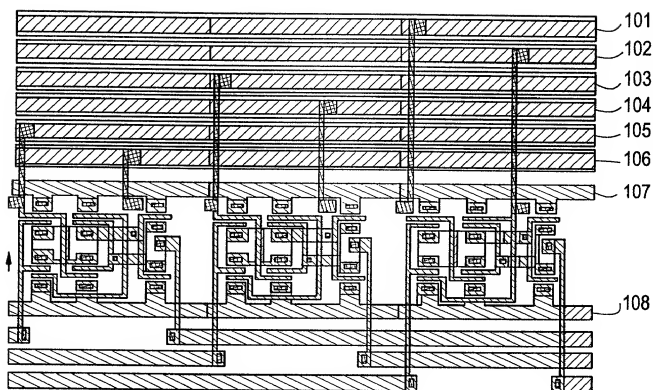


FIG. 2
PRIOR ART

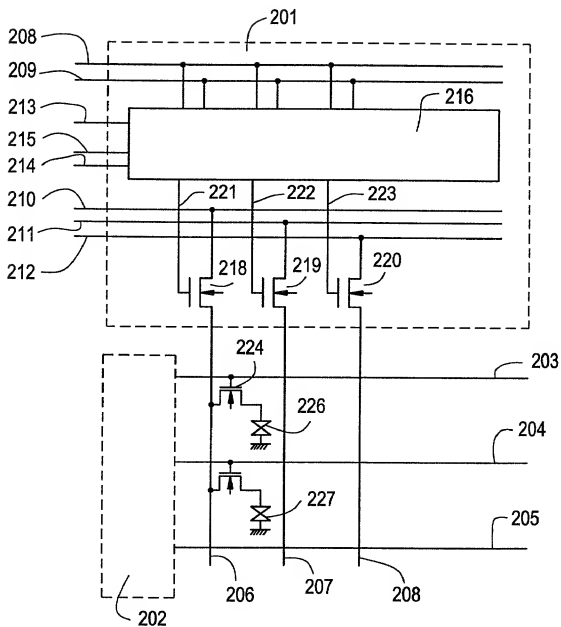


FIG. 3
PRIOR ART

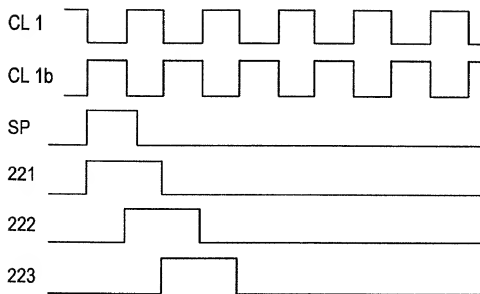


FIG.4A
PRIOR ART

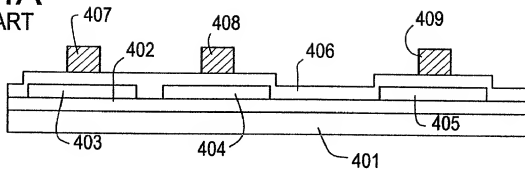


FIG.4B
PRIOR ART

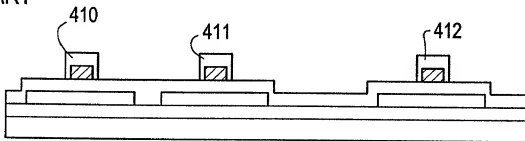


FIG.4C
PRIOR ART

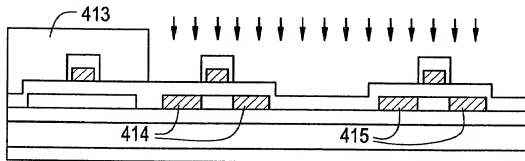


FIG.4D
PRIOR ART

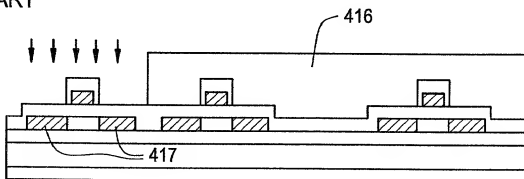


FIG. 5A
PRIOR ART

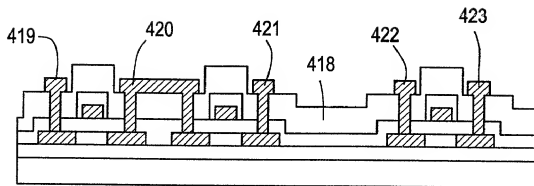
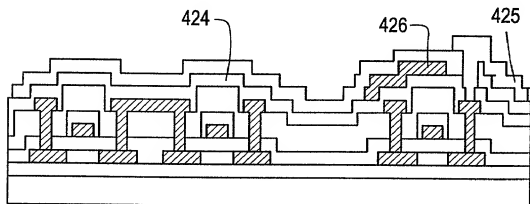


FIG. 5B
PRIOR ART



DRIVER CIRCUIT
TFT



PIXEL TFT

FIG. 6
PRIOR ART

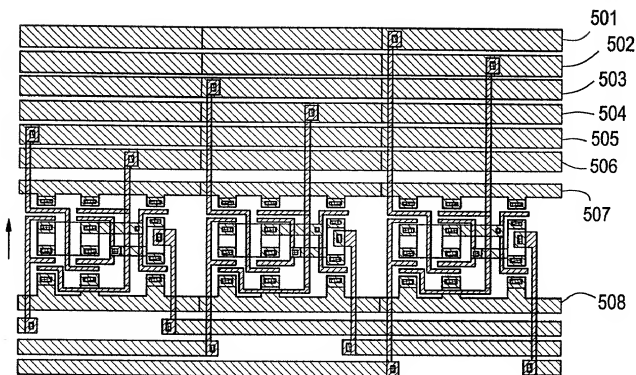
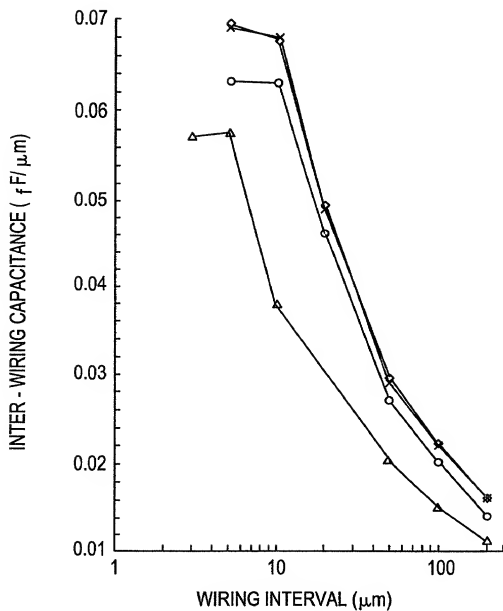


FIG. 7



- △— between Wiring A1 and Gate A1 (width 10 μm)
- ◇— between Wiring A1 and Gate A1 (width 30 μm)
- between Wiring A1s (width 30 μm)
- ×— between Gate A1s (width 30 μm)

FIG. 8

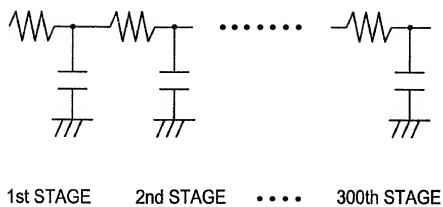


FIG. 9

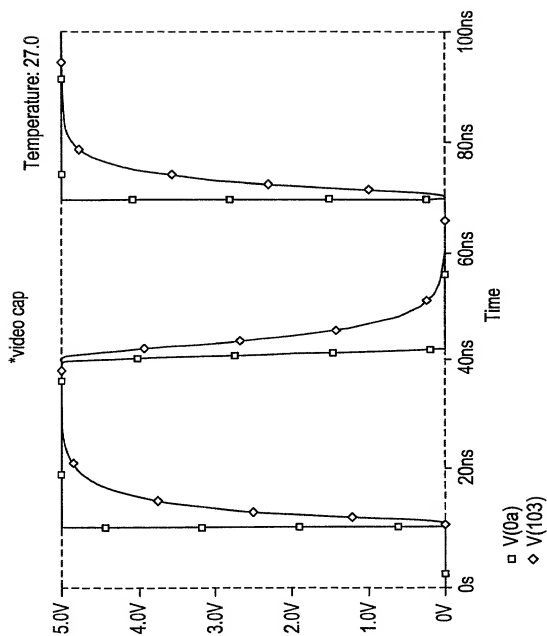


FIG. 10

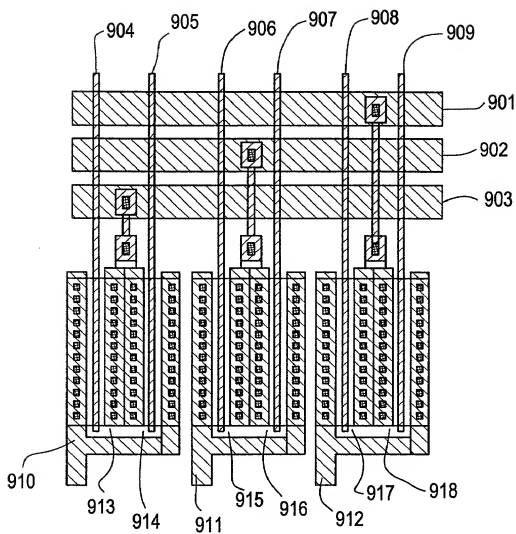


FIG. 11

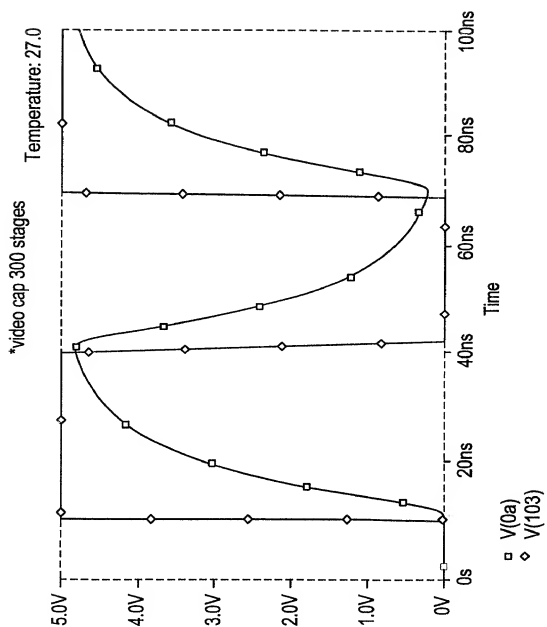


FIG. 12A

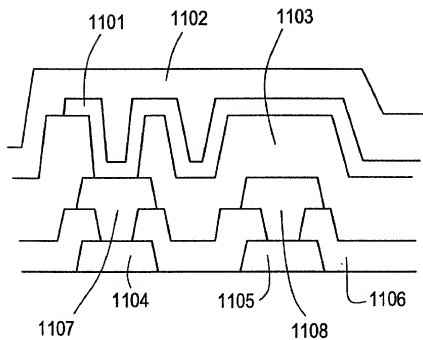


FIG. 12B
PRIOR ART

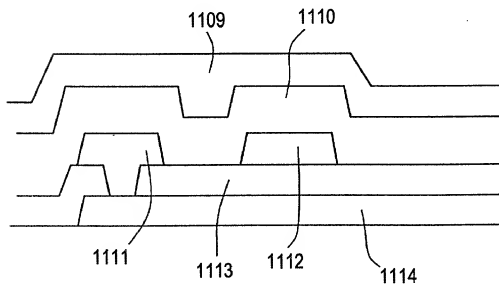


FIG. 13

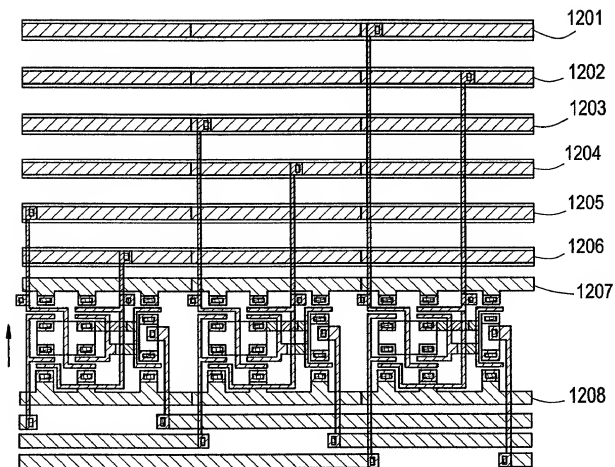


FIG. 14

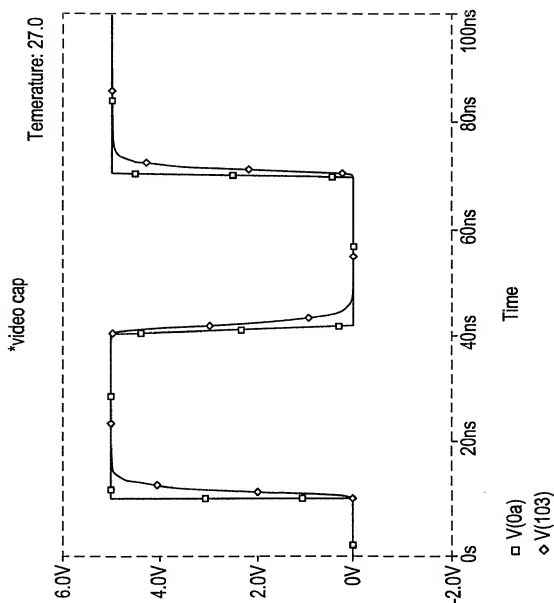


FIG. 15

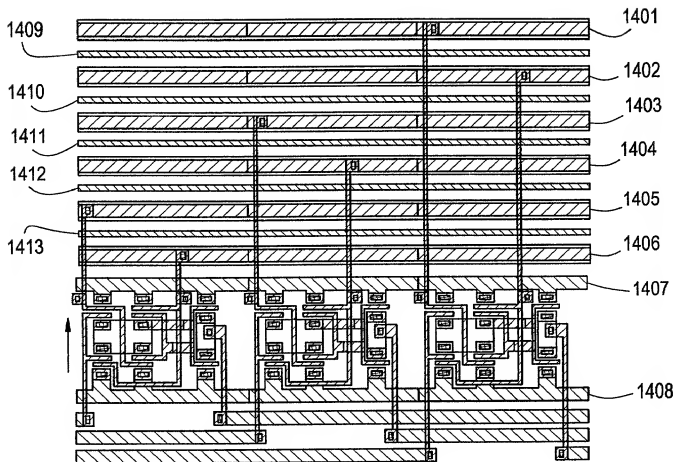


FIG. 16

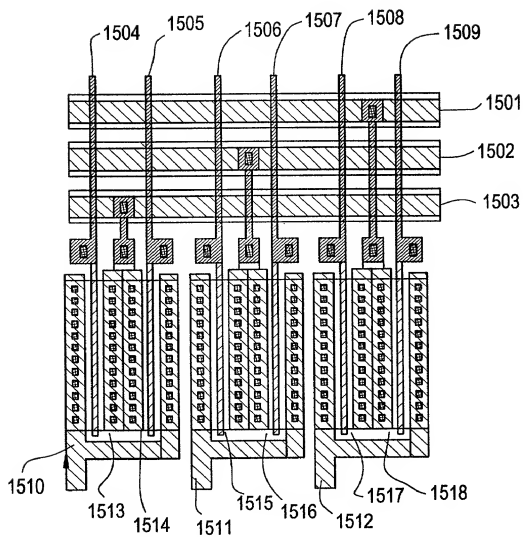
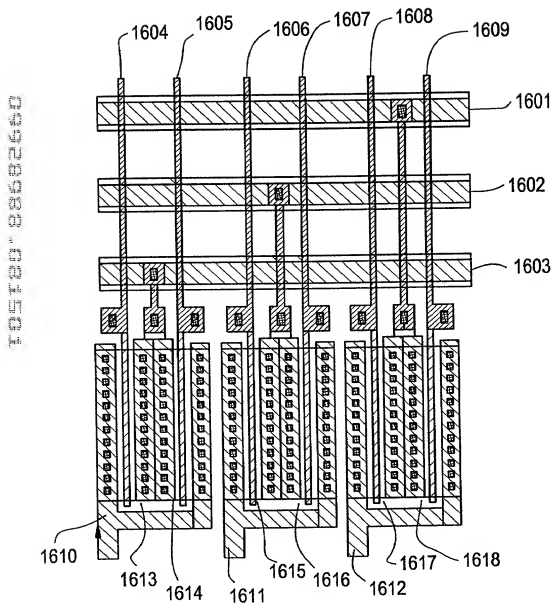


FIG. 17



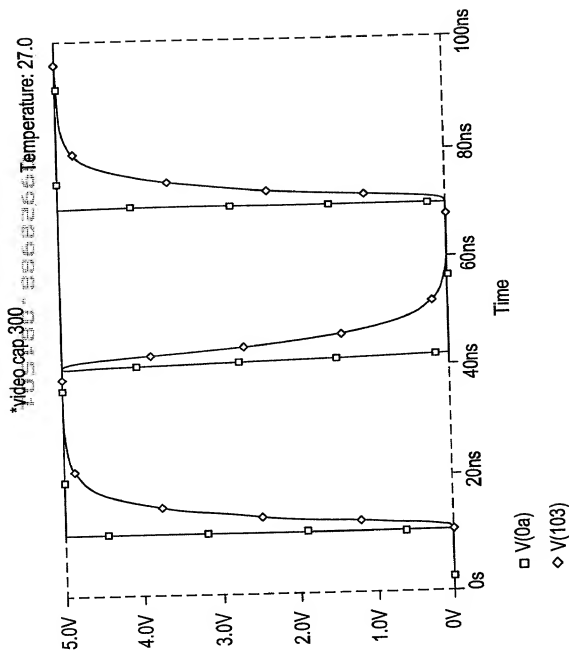


FIG. 19

